

INFORMATION DISCLOSURE STATEMENT

Applicant : Tetsuo SHIMOMURA, et al.
App. No : 10/598,717
Filed : September 8, 2006
For : POLISHING PAD AND
SEMICONDUCTOR DEVICE
MANUFACTURING METHOD
Examiner : Alvin J. Grant
Art Unit : 3723

**CERTIFICATE OF EFS WEB
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I hereby certify that this correspondence, and any other attachment noted on the automated Acknowledgement Receipt, is being transmitted from within the Pacific Time zone to the Commissioner for Patents via the EFS Web server on:

February 6, 2008

(Date)



Katsuhiro Arai, Reg. No. 43,315

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application is a PTO/SB/08 Equivalent listing one (1) reference to be considered by the Examiner. Also enclosed is a copy of the office action with a partial English translation issued by the Chinese Patent Office on December 14, 2007 in a Chinese counterpart of the above-identified patent application. One of the references cited in the office action is already made of record.


This Information Disclosure Statement is being filed before the mailing date of a final action and before the mailing date of a Notice of Allowance.

CERTIFICATION UNDER 37 C.F.R. § 1.97(c) (1)

I hereby certify that each item of information contained in this Statement was first cited in a communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.

Thus, no fee is required as set forth in 37 C.F.R. § 1.97(c), however, the Commissioner is hereby authorized to charge any fees which may be required to Account No. 11-1410.

Respectfully submitted,
KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: February 6, 2008By: 

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